

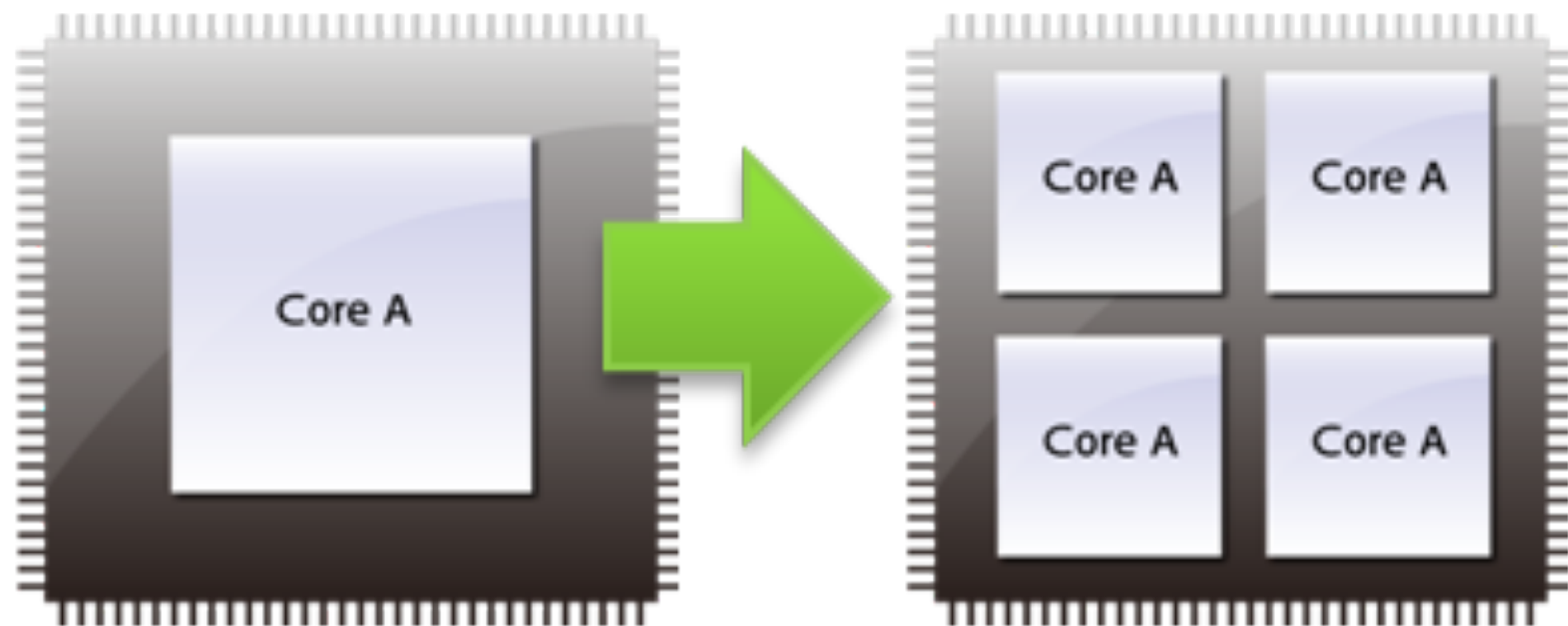
# Multi-Core Data Flow Analysis

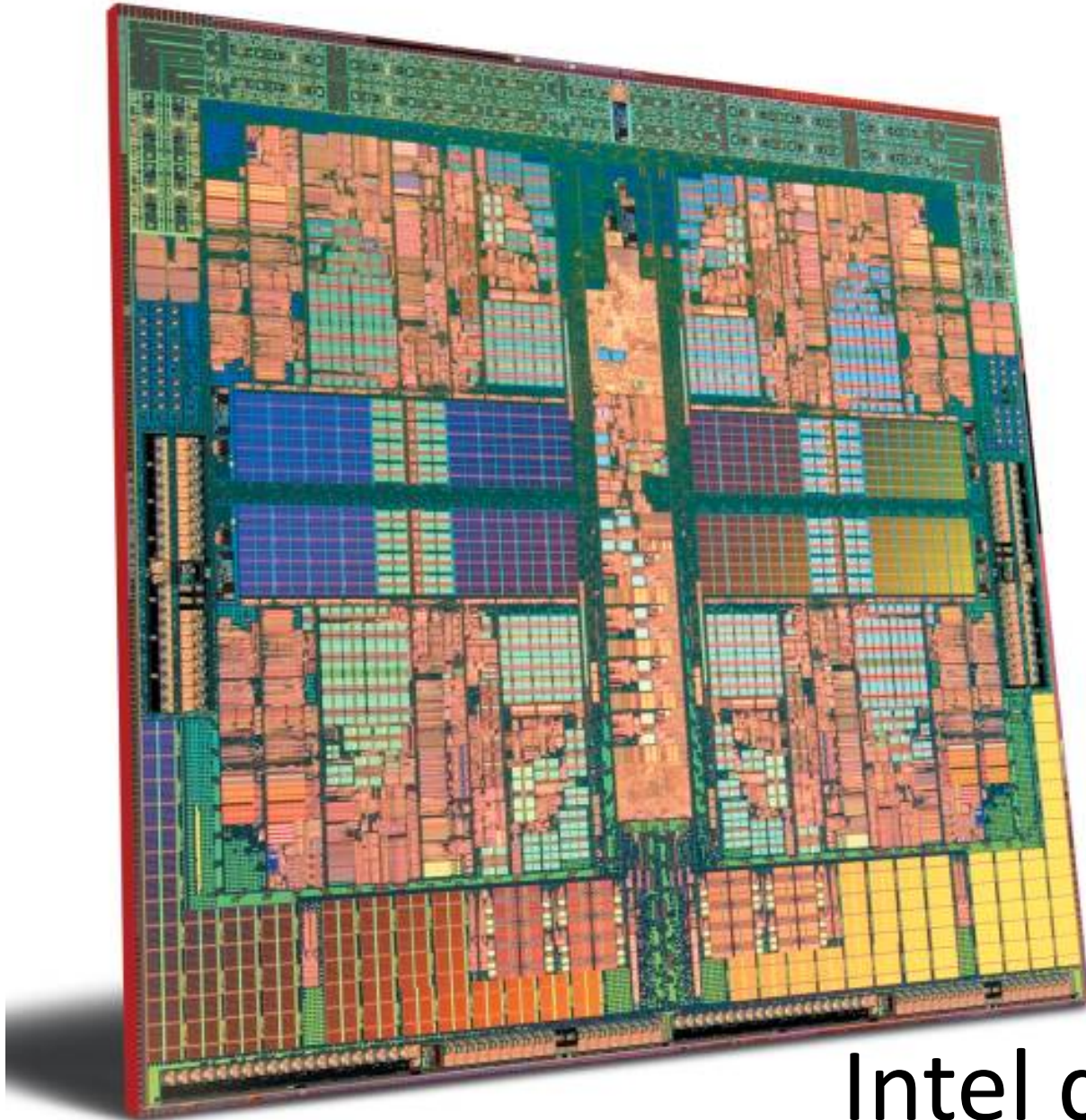
Jeremy Singer

Glasgow

Martin Ward

De Montford





Intel quad-core









Amazon.co.uk: multithreading: Books

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**Any Release Date**

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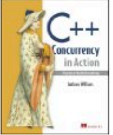
Next 90 days (6)

**Department**

< Any Department

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- Computing & Internet (1,694)
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- 

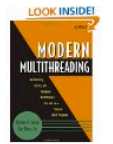
**C++ Concurrency in Action: Practical Multithreading** by Anthony Williams (**Paperback** - 28 Feb 2011)

Buy new: ~~£50.99~~ **£43.34**

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- 

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★★★★☆ (1)

**Excerpt - Front Cover:** "**MULTITHREADING** Implementing, Testing, and Debugging Multithreaded Java and C++ Pthreads Winn Programs Richml H. Carver & Kuo-Chung Tai"

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- 

**Concurrent Programming on Windows: Architecture, Principles, and Patterns (Microsoft .Net Development)** by Joe Duffy and Herb Sutter (**Paperback** - 28 Oct 2008)

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★★★★★ (3)

**Excerpt - Front Matter:** "... book covers all of these areas. When you begin using **multithreading** throughout an application, the importance of clean architecture and design is critical"

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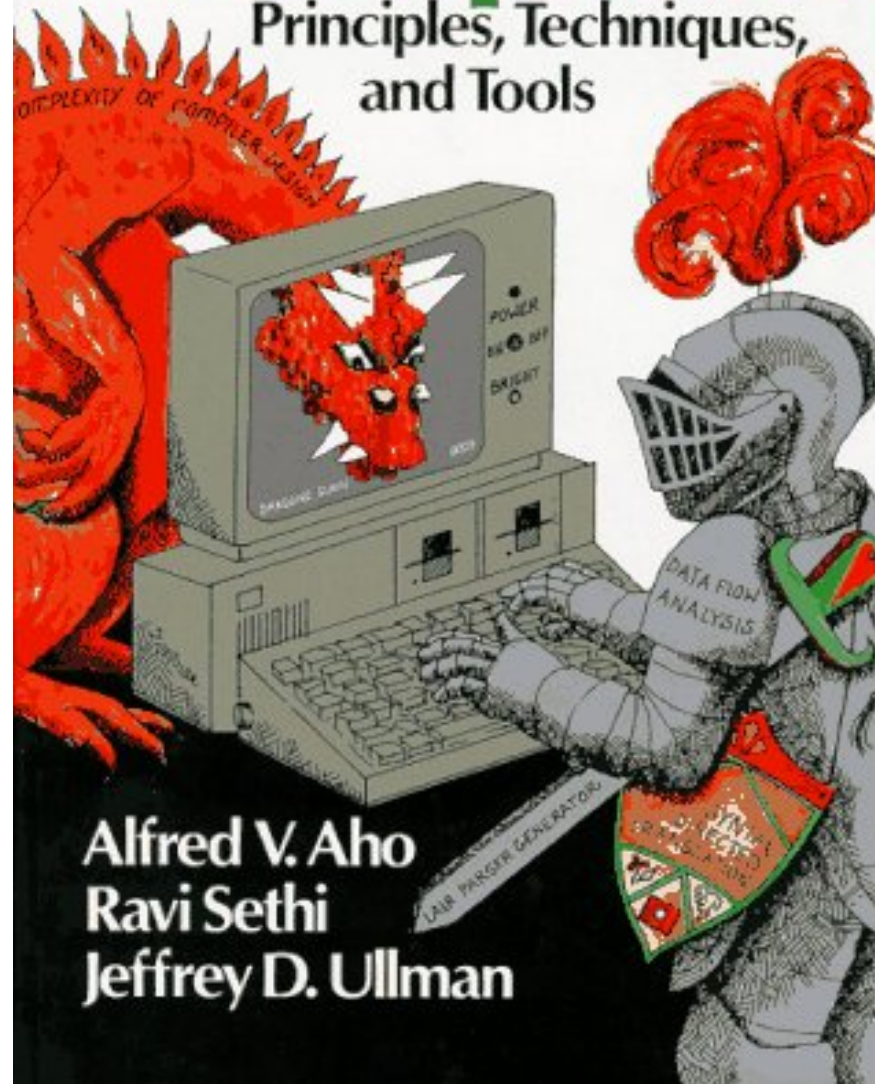
- 

**The Art of Multiprocessor Programming** by Maurice Herlihy and Nir Shavit (**Paperback** - 29 Apr 2008)

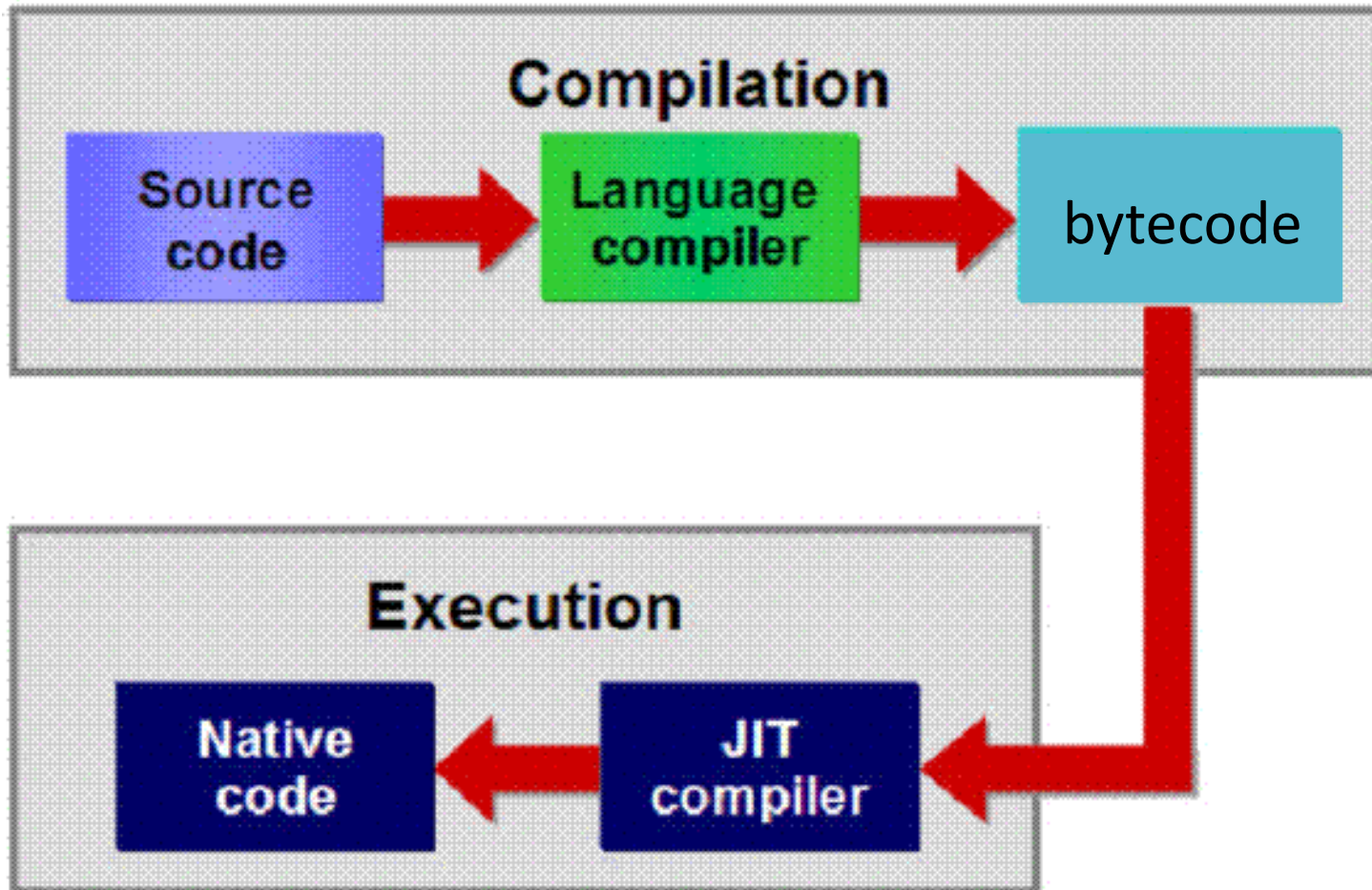
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# Compilers

Principles, Techniques,  
and Tools



Alfred V. Aho  
Ravi Sethi  
Jeffrey D. Ullman







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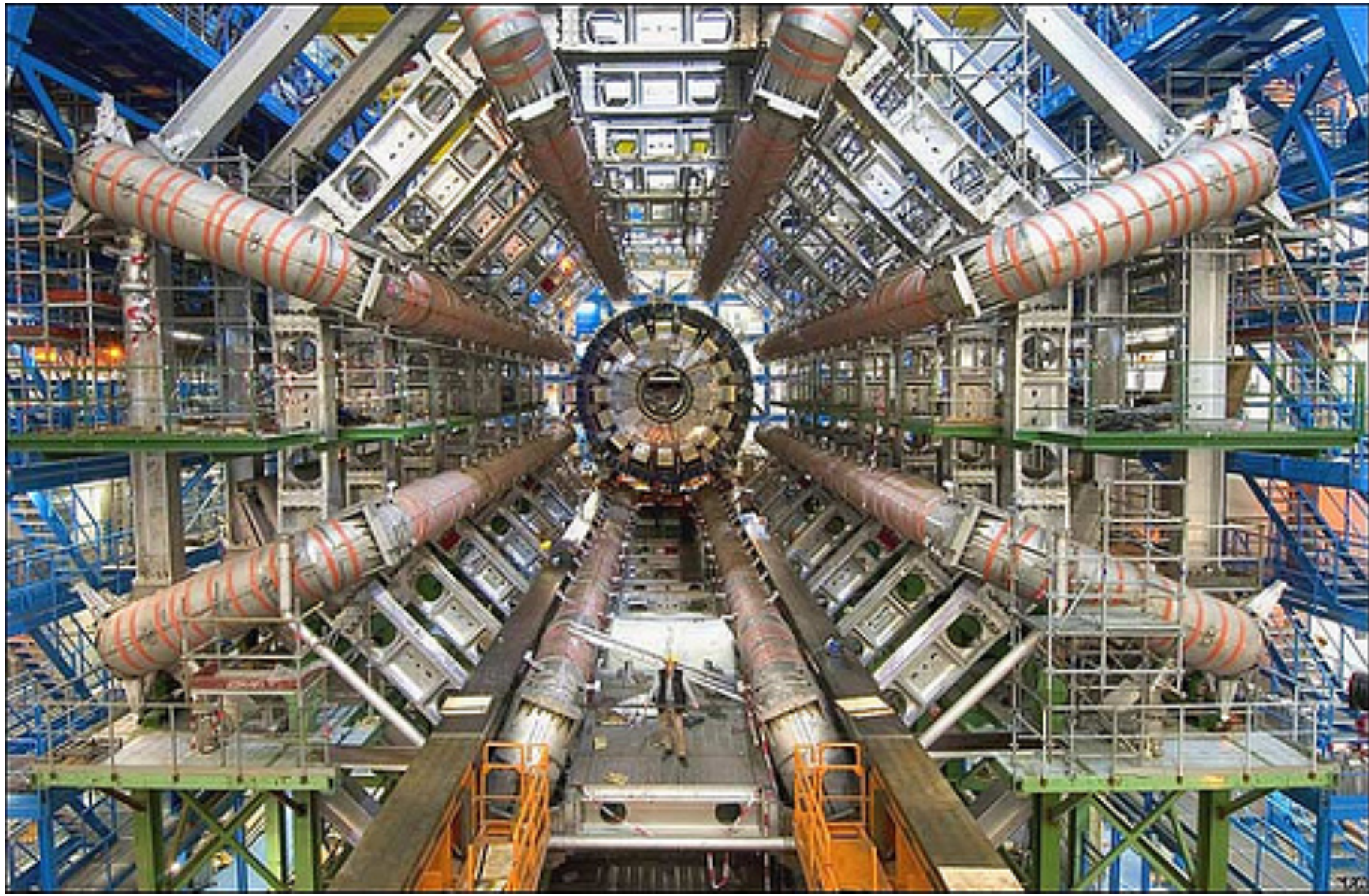
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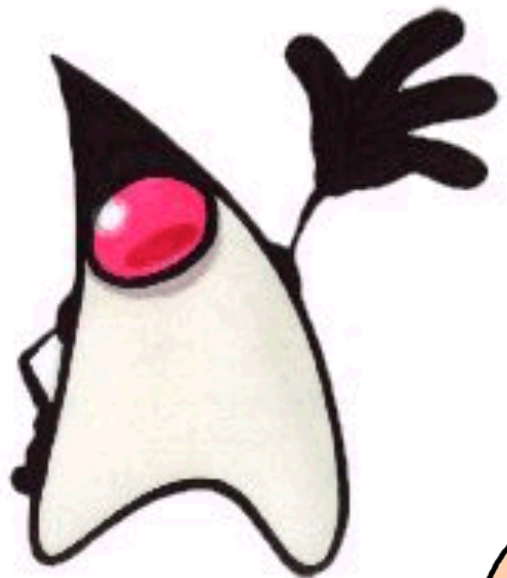
### distcc Monitor - mbp@vexed



Host	Slot	File	State	Tasks
localhost	1	fork.c	Compile	
nevada	0	ialloc.c	Compile	
nevada	1	crc32.c	Compile	
nevada	2	vm86.c	Compile	
nevada	3	datagram.c	Preprocess	
proforma	0	loop.c	Compile	
proforma	1	slab.c	Receive	
proforma	2	pageattr.c	Preprocess	

Load average: 3.31, 1.96, 1.83





# Static Single Assignment Form

- A program is defined to be in SSA form if each variable is a target of exactly one assignment statement in the program text.

# Static Single Assignment Form

- A program is defined to be in SSA form if each **variable** is a target of **exactly one** assignment statement in the **program text**.



# Static Single Assignment Form

- A program is defined to be in SSA form if each variable is a target of exactly one assignment statement in the program text.

**x := 1**

**y := 2**

**x := y**

**x**<sub>1</sub> := 1

**y**<sub>1</sub> := 2

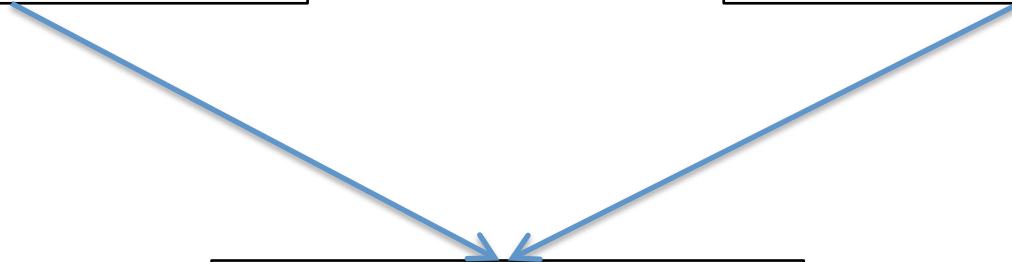
**x**<sub>2</sub> := **y**<sub>1</sub>



**x := 1**

**x := 2**

**y := x+1**



$x_1 := 1$

$x_2 := 2$

$y := x_? + 1$

```
graph TD; A["x1 := 1"] --> C["y := x? + 1"]; B["x2 := 2"] --> C;
```

The diagram illustrates a control flow structure. At the top, two parallel assignment statements are shown in separate boxes:  $x_1 := 1$  on the left and  $x_2 := 2$  on the right. Blue arrows from both boxes point downwards to a single box at the bottom containing the assignment  $y := x_? + 1$ . The subscript  $?$  in the bottom box indicates a conditional or branching operation that depends on the state of  $x_1$  and  $x_2$ .

$x_1 := 1$

$x_2 := 2$

$x_3 := \varphi(x_1, x_2)$   
 $y := x_3 + 1$

# SSA Construction Algorithm

- start with control flow graph derived from program
- Variable names from orig program, and compiler-generated temporaries
- produce control flow graph with SSA property

# Two phases for construction

1. insert  $\varphi$ -functions
2. rename variables

# High-level $\varphi$ -function insertion

- for each variable  $x$ 
  - for each definition of  $x$ 
    - follow control flow paths from  $def$ ,
    - at each merge point where  $def$  is no longer the only definition of  $x$  in scope, insert a  $\varphi$ -function for  $x$



# Actual $\phi$ -function insertion algorithm

---

**Algorithm 1** Classical  $\phi$ -function insertion algorithm

---

```
1:  $W \leftarrow \{\}$ 
2: for all  $v$  : variable names in original program do
3:   for all  $d$  : definition statements of variable  $v$  do
4:     let  $B$  be the basic block containing  $d$ 
5:      $W \leftarrow W \cup \{B\}$ 
6:   end for
7:   while  $W \neq \{\}$  do
8:     remove a basic block  $X$  from  $W$ 
9:     for all  $Y$  :  $block \in DF(X)$  do
10:      if  $Y$  does not contain a  $\phi$ -function for  $v$  then
11:        add  $v \leftarrow \phi(\dots)$  at start of  $Y$ 
12:        if  $Y$  has not already been processed in  $W$  then
13:           $W \leftarrow W \cup \{Y\}$ 
14:        end if
15:      end if
16:    end for
17:   end while
18: end for
```

---

# Actual $\phi$ -function insertion algorithm

---

Algorithm **parallel**  $\phi$ -function insertion algorithm

---

```
1:  $W \leftarrow \{\}$ 
2: DOALL: variable names in original program do
3:   for all  $d$  : definition statements of variable  $v$  do
4:     let  $B$  be the basic block containing  $d$ 
5:      $W \leftarrow W \cup \{B\}$ 
6:   end for
7:   while  $W \neq \{\}$  do
8:     remove a basic block  $X$  from  $W$ 
9:     for all  $Y : block \in DF(X)$  do
10:      if  $Y$  does not contain a  $\phi$ -function for  $v$  then
11:        add  $v \leftarrow \phi(\dots)$  at start of  $Y$ 
12:        if  $Y$  has not already been processed in  $W$  then
13:           $W \leftarrow W \cup \{Y\}$ 
14:        end if
15:      end if
16:    end for
17:  end while
18: ENDDO
```

---

# High-level renaming algorithm

- have an int counter for each orig var:  $\text{Count}(v)$
- have a stack of new vars for each orig var:  $\text{Stack}(v)$
- go through program statements in order
- At def of  $x$ , increment  $\text{Count}(x)$ , push  $x_{\text{count}(x)}$  onto  $\text{Stack}(x)$ , rename  $x$  to  $\text{Stack}(x)$
- At use of  $x$ , rename  $x$  to  $\text{Stack}(x)$
- When defs go out of scope, pop them off  $\text{Stack}(x)$

# Actual renaming algorithm

---

**Algorithm 3** Classical SSA renaming algorithm

---

```
1: for all  $V$  : variables in original program do  
2:    $Count(V) \leftarrow 0$   
3:    $S(V) \leftarrow \text{EmptyStack}$   
4: end for  
5: call Search(EntryNode)  
6:
```

```
6:
7: procedure Search( $X$  : BasicBlock)
8: for all  $A$  : statement in  $X$  do
9:   if  $A$  is not a  $\phi$ -function then
10:    for all  $u$  : variables used in  $A$  do
11:      replace use of  $u$  with  $S(u)$  in  $A$ 
12:    end for
13:  end if
14:  for all  $v$  : variables defined in  $A$  do
15:     $i \leftarrow \text{Count}(v)$ 
16:    replace definition of  $v$  with  $v_i$  in  $A$ 
17:    push  $v_i$  onto  $S(v)$ 
18:     $\text{Count}(v) \leftarrow i + 1$ 
19:  end for
20: end for
21: for all  $Y \in \text{Succ}(X)$  do
22:   let  $j$  be the index of the  $\phi$ -function operands in  $Y$  that correspond to basic block
    $X$ 
23:   for all  $F$ :  $\phi$ -function in  $Y$  do
24:     let  $V$  be the  $j$ th operand in  $F$ 
25:     replace  $V$  with  $S(V)$  at the  $j$ th operand in  $F$ 
26:   end for
27: end for
28: for all  $Z \in \text{Children}(X)$  do
29:   call Search( $Z$ )
30: end for
31: for all  $A$  : statements in  $X$  do
32:   for all  $V_i$  : variables defined in  $A$  do
33:     let  $V$  be the original variable corresponding to  $V_i$ 
34:     pop  $S(V)$ 
35:   end for
36: end for
37: end procedure
```

---



```

6:
7: procedure Search( $X$  : BasicBlock)
8: for all  $A$  : statement in  $X$  do
9:   if  $A$  is not a  $\phi$ -function then
10:    for all  $u$  : variables used in  $A$  do
11:      replace use of  $u$  with  $S(u)$  in  $A$ 
12:    end for
13:  end if
14:  for all  $v$  : variables defined in  $A$  do
15:     $i \leftarrow \text{Count}(v)$ 
16:    replace definition of  $v$  with  $v_i$  in  $A$ 
17:    push  $v_i$  onto  $S(v)$ 
18:     $\text{Count}(v) \leftarrow i + 1$ 
19:  end for
20: end for
21: for all  $Y \in \text{Succ}(X)$  do
22:   let  $j$  be the index of the  $\phi$ -function operands in  $Y$  that correspond to basic block  $X$ 
23:   for all  $F$ :  $\phi$ -function in  $Y$  do
24:     let  $V$  be the  $j$ th operand in  $F$ 
25:     replace  $V$  with  $S(V)$  at the  $j$ th operand in  $F$ 
26:   end for
27: end for
28: doall  $Z \in \text{Children}(X)$  do
29:   call Search( $Z$ )
30: enddo
31: for all  $A$  : statements in  $X$  do
32:   for all  $V_i$  : variables defined in  $A$  do
33:     let  $V$  be the original variable corresponding to  $V_i$ 
34:     pop  $S(V)$ 
35:   end for
36: end for
37: end procedure

```

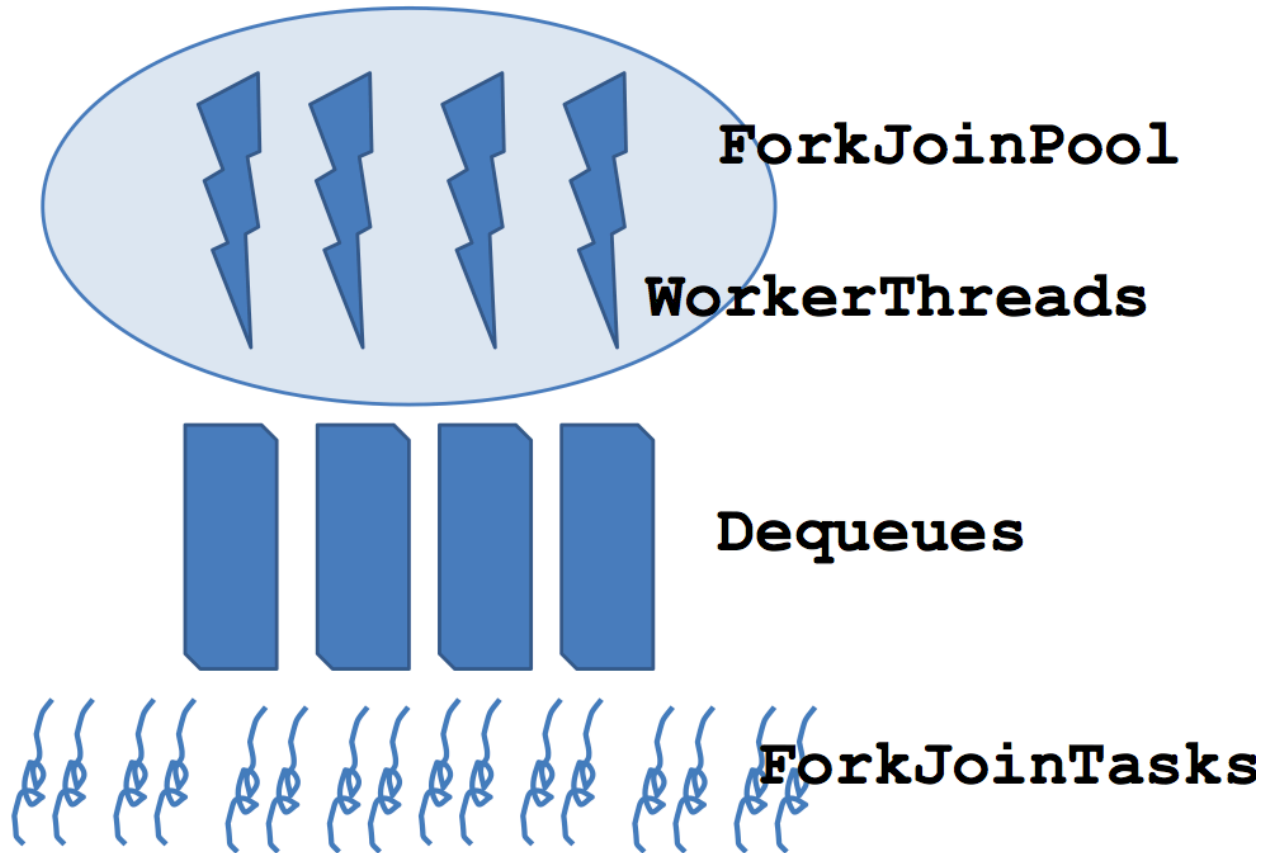
synchronize Count

privatize S

# Implementation Details

- Algorithms implemented in Soot
  - a Java bytecode compiler framework
- Parallelism via Java fork/join framework
  - thread pool
  - lightweight tasks
  - work-stealing queues
- Thread-safe data structures
  - `java.util.concurrent.ConcurrentHashMap`

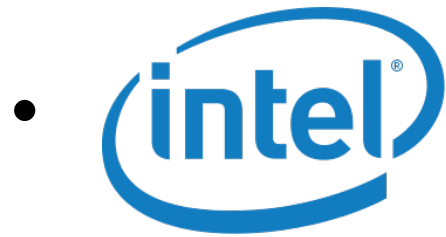
# jsr166y



# Evaluation

- Use standard Java benchmark programs
  - DaCapo, Java Grande
- Problem – some methods are so small that the parallel algorithm performs worse than the sequential one
- Solution – have a method size threshold, below which we always use sequential algorithm, above which we use parallel

# Evaluation Platform



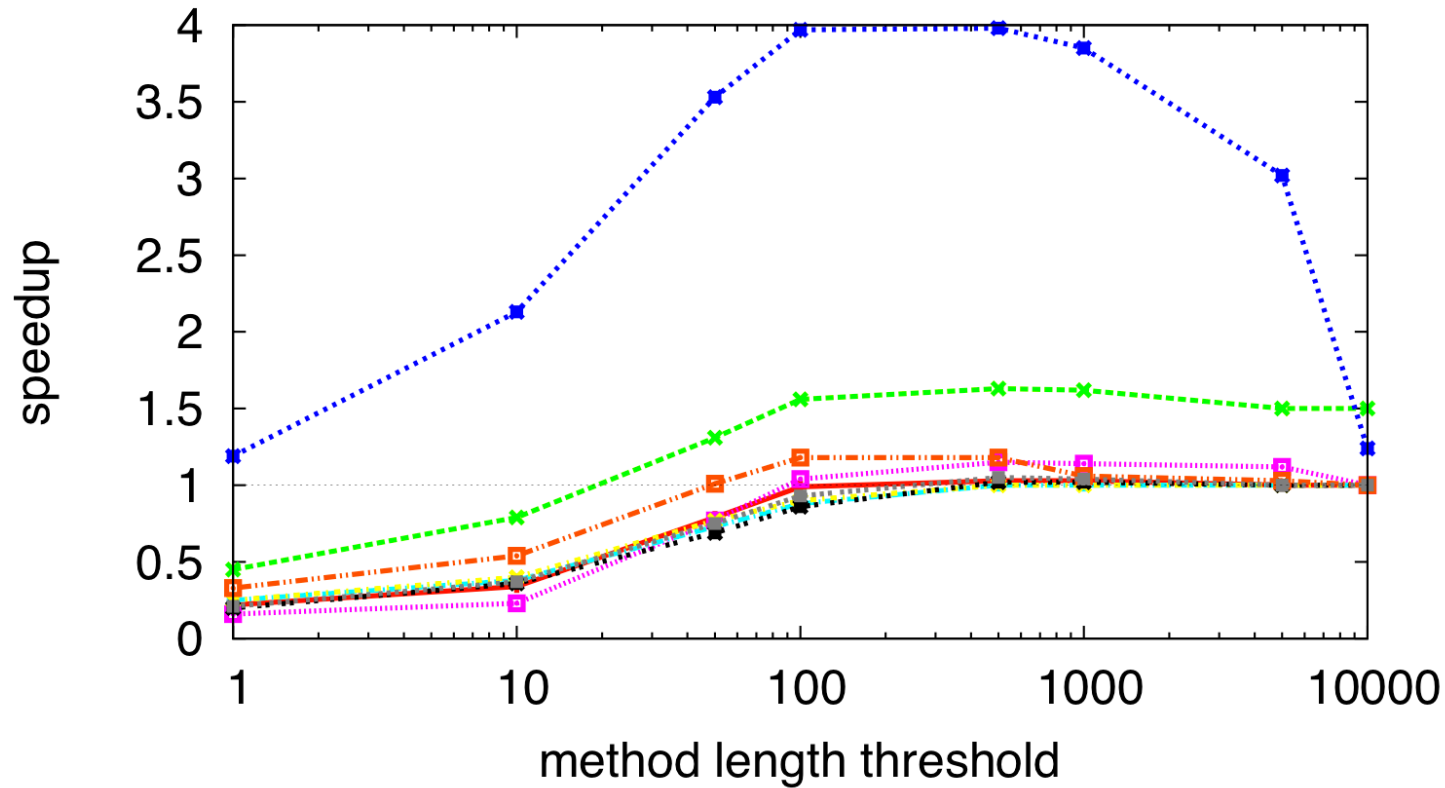
Core i7-920

- 4 cores x 2 contexts

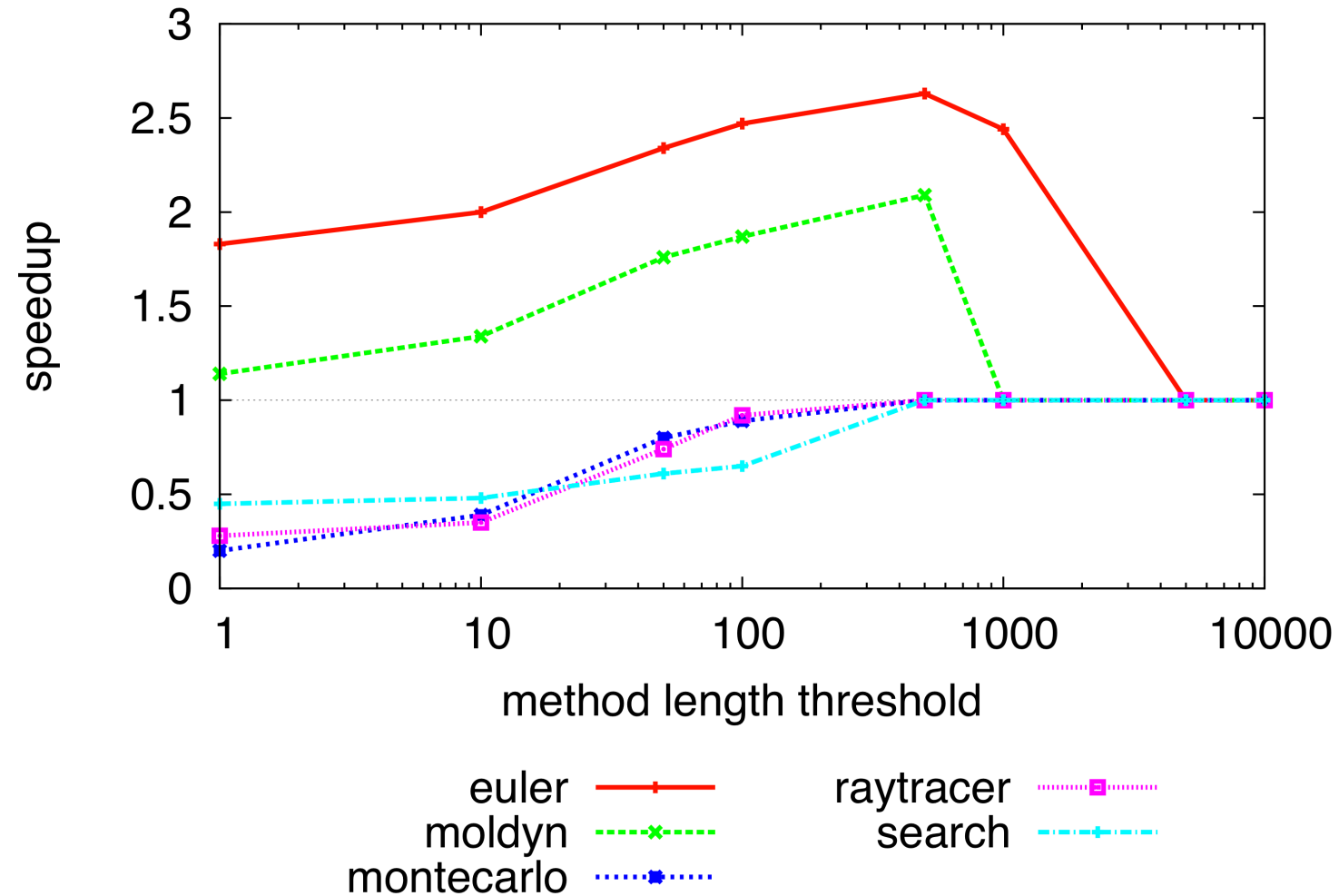
- JVM – 1.6, Hotspot v14.0-b16
- Soot – v2.4.0
- Linux – x86\_64 v2.6.31



# SSA Speedup on DaCapo



# SSA Speedup on Java Grande



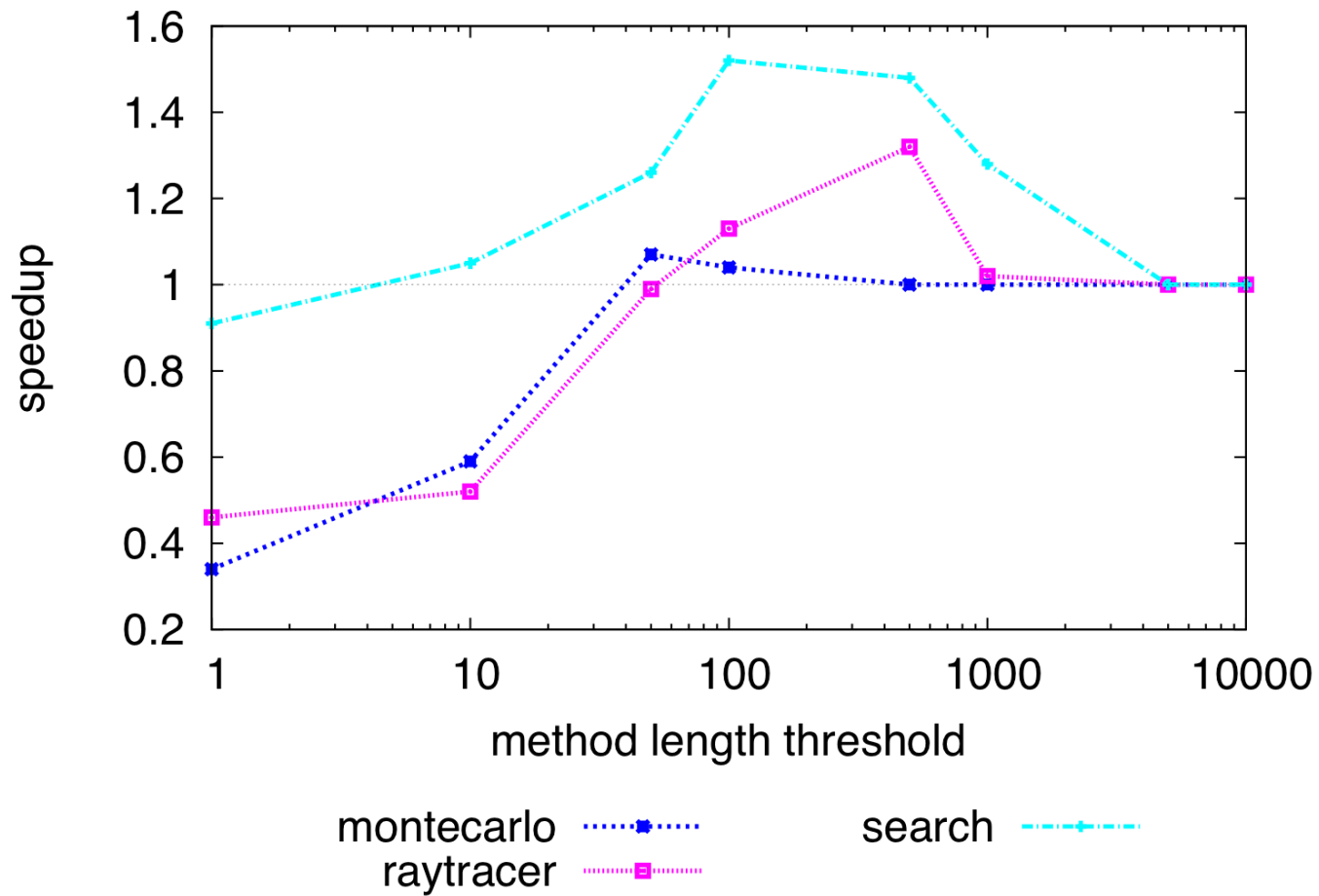
# Explain with Method Size Stats

	<i>app</i>	<i>description</i>	<i># methods</i>	<i>mean length</i>	<i>max length</i>
DaCapo	avrora	program simulator	2836	21.9	1083
	batik	SVG image processing	7137	34.3	41033
	fop	PDF generator	6749	44.5	33089
	jython	Python interpreter	20664	25.4	7846
	luindex	text indexing	1885	30.6	493
	lusearch	text search	1613	26.9	1187
	pmd	static analysis	6477	33.6	2881
	sunflow	raytracer	1109	50.4	6308
	xalan	XML parser	6189	29.5	2881
Java Grande	euler	fluid dynamics	27	295.81	1822
	moldyn	molecular dynamics simulation	20	102.20	931
	montecarlo	Monte Carlo simulation	178	17.65	211
	raytracer	raytracer	65	30.63	229
	search	alpha-beta search	29	86.34	465

```
private static byte lineBreakProperties[][] = new byte[512][];

private static void init_0() {
    lineBreakProperties[0] = new byte[] { 9,9,9,9,9,9,9,9,9,4,22,6,6,10,9,9,9,9,9,9,
    lineBreakProperties[1] = new byte[] { 9,9,9,9,9,23,9,9,9,9,9,9,9,9,9,9,9,9,9,9,9,
    lineBreakProperties[2] = new byte[] { 2,2,2,2,2,2,2,2,2,2,2,2,2,2,2,2,2,2,2,2,2,
    lineBreakProperties[5] = new byte[] { 2,2,2,2,2,2,2,2,2,2,2,2,2,2,2,2,2,2,2,2,2,
    lineBreakProperties[6] = new byte[] { 9,9,9,9,9,9,9,9,9,9,9,9,9,9,9,9,9,9,9,9,9,
    lineBreakProperties[7] = new byte[] { 0,0,0,0,2,2,2,2,2,2,2,0,2,0,2,2,2,2,2,2,2,2,
    lineBreakProperties[9] = new byte[] { 2,2,2,9,9,9,9,0,9,9,2,2,2,2,2,2,2,2,2,2,2,2,2,
    lineBreakProperties[10] = new byte[] { 2,2,2,2,2,2,2,2,2,2,2,2,2,2,2,2,2,2,2,2,2,2,
    lineBreakProperties[11] = new byte[] { 2,2,2,2,2,2,2,2,0,18,4,0,0,0,0,0,0,9,9,9,9,
    lineBreakProperties[12] = new byte[] { 2,2,2,2,0,0,0,0,0,0,0,0,27,11,18,2,2,9,9,9,9,
    lineBreakProperties[13] = new byte[] { 2,2,2,2,2,2,2,2,2,2,2,2,2,2,2,2,2,2,2,2,2,2,
    lineBreakProperties[14] = new byte[] { 2,2,2,2,2,2,2,2,2,2,2,2,2,2,0,2,2,9,2,2,2,2,
    lineBreakProperties[15] = new byte[] { 2,2,2,2,2,2,2,2,2,2,2,2,2,2,2,2,2,2,2,2,2,2,
    lineBreakProperties[16] = new byte[] { 0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,
    lineBreakProperties[18] = new byte[] { 0,9,9,9,2,2,2,2,2,2,2,2,2,2,2,2,2,2,2,2,2,2,2,
    lineBreakProperties[19] = new byte[] { 0,9,9,9,0,2,2,2,2,2,2,2,2,2,0,0,2,2,0,0,2,2,2,
    lineBreakProperties[20] = new byte[] { 0,9,9,9,0,2,2,2,2,2,2,2,0,0,0,0,2,2,0,0,2,2,2,
    lineBreakProperties[21] = new byte[] { 0,9,9,9,0,2,2,2,2,2,2,2,2,2,2,0,2,2,2,0,2,2,2,
    lineBreakProperties[22] = new byte[] { 0,9,9,9,0,2,2,2,2,2,2,2,2,2,0,0,2,2,0,0,2,2,2,
    lineBreakProperties[23] = new byte[] { 0,0,9,2,0,2,2,2,2,2,2,2,0,0,0,2,2,2,0,2,2,2,2,2,
```

# Effects of Method Inlining



# Throw rotten fruit now



- Most methods are too short for parallel algorithm.
- SSA construction time is insignificant in overall compilation process.
- Why not parallelize SSA construction for multiple methods at once?



# Concluding Remarks

- We have presented one technique for parallelization of data flow analysis, to take advantage of multicore resources.
- We see overhead of fork/join parallelism versus saving of parallel execution – need to find threshold.